

SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): 志盛翔 (日期):2020-4-2

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : GT $35V2200\mu F(\phi 16x25)$

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPLI	IER
PREPARED (拟定)	CHECKED (审核)
赵安平	刘渭清

CUSTO	OMER
APPROVAL (批准)	SIGNATURE (签名)

ELECTROLYTIC CAPACITOR SPECIFICATION GT SERIES

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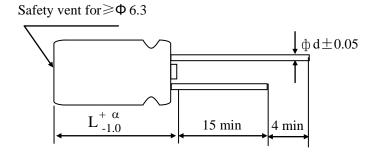
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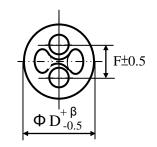
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Table 1 Product Dimensions and Characteristics

Unit: mm





α	L<20 : α=1.5; L≥20 : α=2.0
β	$\Phi D < 20 : \beta = 0.5; \ \Phi D \ge 20 : \beta = 1.0$

* If it is flat rubber, there is no bulge from the flat rubber surface.

N		SAMXON	WV	Cap.	Cap	Temp.	tan δ	Leakage	Max Ripple Current at 105℃	Impedance At 20℃	Load		ension (mm)		C1
N	0.	Part No.	Vdc	(μF)	tolerance	range(℃)	(120Hz, 20℃)	Current (µA,2min)	100KHz (mA rms)	100KHz (Ωmax)	lifetime (Hrs)	D×L	F	фd	Sleeve
1	1	EGT228M1VK25RR**P-R	35	2200	-20%~+20%	-40~105	0.14	770	2552	0.028	10000	16X25	7.5	0.8	PET

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1. Application

This specification applies to polar Aluminum electrolytic capacitor (foil type) used in electronic equipment. Designed capacitor's quality meets IEC60384.

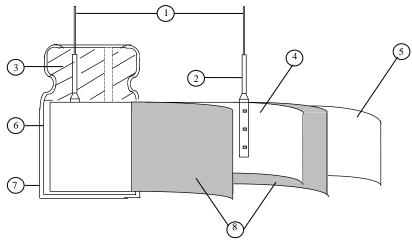
Part Number System 2. $\lceil 7 \rceil$ 101112 1 2 3 4 5 6 8 9 13 14 1516 EGS 1 0 М SAMXON SLEEVE PRODUCT LINE MATERIAL SERIES CAPACITANCE VOLTAGE CASE SIZE Cap(MFD) Code Tolerance (%) Code Voltage (W.V.) Code Feature Code B 1 C D 0D For internal use only Radial bulk 104 0.1 ±5 2.5 0E (The product lines 0G we have H,A,B,C,D, Ammo Taping 0.22 224 K 6.3 OJ E,M or 0,1,2,3,4,5,9). ±10 0K 0.33 2.0mm Pitch 10 1A ±15 L 12.5 1B TU 2.5mm Pitch 16 1C M 20 1D ±20 TV 105 3.5mm Pitch 1E 30 11 5.0mm Pitch PET Р 2.2 225 Ν +3032 13 35 1V Lead Cut & Form 3.3 335 w 40 1G СВ 42 1M CB-Type 4.7 475 Α 50 1H CE CE-Type 57 1L 10 106 -20 +10 63 1J С HE-Type ΗE 226 18 22 71 75 1**T** -20 +40 × KD-Type 336 33 80 1K 85 1R -20 +50 s FD-Type FD 47 476 90 19 5 05 5 07 7 07 7.7 77 100 2A EΗ -10 0 EH-Type В 100 120 20 125 2B PCB Termial -10 +20 v 150 2ZT2 11 1A 12 1B 13 1C 20 25 2J 30 3A 35 3E 160 2C sw -10 +30 330 337 Q 180 2P 200 2D Snap-in sx 470 477 -10 +50 215 22 т 220 2N sz 2200 228 -5 +10 230 23 Е 250 2E Lug SG 22000 229 275 2T -5 +15 F 300 21 05 33000 339 -5 +20 310 2R G 06 315 2F 47000 479 0 +20 330 2U Т5 350 2V 100000 10T Screw +30 360 2X Т6 375 2Q 15T 150000 385 2Y 0 +50 D5 400 2G 220000 22T +5 +15 420 2M z D6 450 2W 330000 33T +5 +20 500 2H D 550 25 1000000 10M +10 +50 Υ 600 26 630 1500000 15M +10 +30 н 2200000 22M 3300000 33M

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3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



No	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

4. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C
Relative humidity : 45% to 85%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature $: 20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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	ITEM				PERFO	RMANC	Έ			
	Rated voltage				<u>-</u>					
	(WV)	WV (V.DC)	6.3	10	16	25	35	50	63	100
4.1		SV (V.DC)	8	13	20	32	44	63	79	125
	Surge voltage (SV)									
4.2	Nominal capacitance (Tolerance)	<condition> Measuring F Measuring V Measuring T <criteria> Shall be with</criteria></condition>	requency oltage emperat	: No ure : 20)±2℃	han 0.5V				
4.3	Leakage current	Condition> Connecting t minutes, and Criteria> Refer to Table	he capac then, me				istor (1	kΩ ±10	Ω) in se	eries for
4.4	tanδ	<condition> See 4.2, Norn <criteria> Refer to Table</criteria></condition>	n Capac	itance, fo	or measur	ing frequ	ency, vo	oltage and	l tempera	ture.
4.5	Terminal strength	0.5r Over 0.	ength of capacitor rength of apacitor, 2~3 seconder of lead num and 15 mm to	Termina applied fonds, and d wire less	Tens	ent the te that it for 90 ile force 1 (kgf) 5 (0.51) 0 (1.0)	rminal (10° to its	1~4 mm toriginal p Bending (kg 2.5 (0)	from the reposition version (see N gf) (0.25) (.51)	rubber) fo

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		<condition< th=""><th>1</th><th>na Tampa</th><th>matuma(°C)</th><th>. </th><th></th><th>Time</th><th></th><th></th></condition<>	1	na Tampa	matuma(°C)	.		Time		
		1		$\frac{\text{ng Temper}}{20\pm 2}$		-	to manah	thermal e	. au ilihai	1177
		2		-40(-25)		_		thermal e	-	
		3		20±2				thermal e		
		4	ļ	105±				thermal e	-	
		5		20 ± 2	2	Time	to reach	thermal e	equilibri	ım
		<criteria:< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></criteria:<>								
	Temperature		hall be with			4.4The le	eakage cı	ırrent me	asured s	hall not
	characteristi		8 times of i	-						
4.6	cs	_	δ , tan δ sha		nin the lin	nit of Iter	n 4.4The	leakage	current	shall no
			the specifie							
			\mathbb{C} (-25°C), in	mpedance	(z) ratio s	shall not e	exceed th	ie value o	of the fol	lowing
		table.		1	1				T	T
		Working V		6.3	10	16	25	35	50	100
		Z-25°C/	Z+20°C	4	3	2	2	2	2	2
		Z-40°C/	Z+20°C	8	6	4	3	3	3	3
		<condition< td=""><td>on></td><td></td><td></td><td>e measur</td><td></td><td></td><td>4 - 4</td><td></td></condition<>	on>			e measur			4 - 4	
47	Load	<condition 105="" according="" and="" dc="" product="" result="" ri="" sh="" shou<="" td="" ±2="" ℃=""><td>on> g to IEC6038 with DC bi ipple peak ould be tested</td><td>34-4No.4.2 as voltage voltage sh</td><td>13 method plus the plus the pl</td><td>ds, The ca rated ripp xceed the</td><td>pacitor is le curren e rated w</td><td>s stored a t for Tab vorking v</td><td>ole 1. (T. voltage)</td><td>he sum o Then th</td></condition>	on> g to IEC6038 with DC bi ipple peak ould be tested	34-4No.4.2 as voltage voltage sh	13 method plus the plus the pl	ds, The ca rated ripp xceed the	pacitor is le curren e rated w	s stored a t for Tab vorking v	ole 1. (T. voltage)	he sum o Then th
4.7	life	Conditional According 105 °C ±2 DC and ripproduct show the conditional according to the conditional	on> g to IEC6038 with DC bi ipple peak ould be tested	34-4No.4 as voltage voltage shed after 16 following	13 method e plus the mall not ex 5 hours red g table:	ds, The ca rated ripp xceed the covering t	pacitor is le curren e rated w ime at at	s stored a t for Tab vorking v	ole 1. (T. voltage)	he sum o Then th
4.7		Condition According 105 ℃ ±2 DC and rip product she result show Criteria The chara	on> g to IEC6038 with DC bit ipple peak ould be tested the peak ould meet the peak ould m	34-4No.4 as voltage voltage sh ed after 16 following	13 method e plus the pall not end hours red to thours red to thours to the pall to the pal	ds, The ca rated ripp xceed the covering t	pacitor is le curren rated w time at at ments.	s stored a t for Tab orking v mospher	ole 1. (T. voltage)	he sum o Then th
4.7	life	<condition 105="" <criteria="" according="" and="" chara="" dc="" lea<="" product="" result="" ri="" sh="" shou="" td="" the="" °c="" ±2=""><td>on> It to IEC6038 It with DC bit ipple peak ould be tested the peak ould meet the peak output to the peak output in the peak ou</td><td>as voltage shed after 16 following</td><td>13 method plus the pall not ex hours red table: e followin</td><td>ls, The ca rated ripp xceed the covering t</td><td>pacitor is le current rated with the at at ments.</td><td>s stored a t for Tab vorking v mospher</td><td>ole 1. (T. voltage)</td><td>he sum o Then th</td></condition>	on> It to IEC6038 It with DC bit ipple peak ould be tested the peak ould meet the peak output to the peak output in the peak ou	as voltage shed after 16 following	13 method plus the pall not ex hours red table: e followin	ls, The ca rated ripp xceed the covering t	pacitor is le current rated with the at at ments.	s stored a t for Tab vorking v mospher	ole 1. (T. voltage)	he sum o Then th
4.7	life	Conditional According 105 °C ±2 DC and riproduct show contains the chara team of the character	to IEC60382 with DC bit ipple peak ould be tested all meet the certistic shaukage current pacitance Ch	as voltage shed after 16 following	13 method e plus the pall not end hours red to hours red to table: e following Value in	ls, The carated ripp exceed the covering to grequire 4.3 shall	pacitor is le current rated write at at ments.	s stored a t for Tab yorking v mospher ied	ole 1. (T. voltage) ic condit	he sum o Then th
4.7	life	Conditional According 105 °C ±2 DC and risproduct shows a contract the charant of the charant	to IEC6038 with DC bitipple peak ould be tested the section of the	as voltage shed after 16 following	13 method plus the pall not examined hours recognized table: e followin Value in Within	ds, The carated ripp exceed the covering to the desired that the desired t	pacitor is le current rated write at at ments. be satisficinitial value of the control of the c	s stored a t for Tab yorking v mospher ied alue.	ole 1. (Tooltage) ic condite	he sum o Then th
4.7	life	Conditional According 105 °C ±2 DC and risproduct shows a contract the charant of the charant	to IEC60382 with DC bit ipple peak ould be tested all meet the certistic shaukage current pacitance Ch	as voltage shed after 16 following	13 method plus the pall not examined hours recognized table: e followin Value in Within	ls, The carated ripp exceed the covering to grequire 4.3 shall	pacitor is le current rated write at at ments. be satisficinitial value of the control of the c	s stored a t for Tab yorking v mospher ied alue.	ole 1. (Tooltage) ic condite	he sum o Then th
4.7	life	Condition According 105 ℃ ±2 DC and riproduct sheer Criteria The chara Captain App Condition Condition The capacit	to IEC6038 with DC bit ipple peak ould be tested the sectoristic shall kage current pacitance Chopearance	as voltage shed after 16 following ll meet the thange	13 method plus the pall not examined the second table: the following table: Within the second the second table in the second t	ds, The carated ripp exceed the covering to th	pacitor is le current rated writine at at ments. be satisficial value of the leakage of the lea	s stored a t for Tab vorking v mospher ied alue. e specifie of electro	d value. lyte.	the sum of the the thickness of the thic

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		<criteria></criteria>	1 6 11
		The characteristic shall meet the	
	Shelf	Leakage current	Value in 4.3 shall be satisfied Within ±25% of initial value.
4.8	life	Capacitance Change	
	test	tanδ	Not more than 200% of the specified value.
		Appearance	There shall be no leakage of electrolyte.
			stored more than 1 year, the leakage current may through about 1 k Ω resistor, if necessary.
			e 15~35℃.
4.0	Surge	Leakage current	Not more than the specified value.
4.9	test	Capacitance Change	Within $\pm 15\%$ of initial value.
		tano	
			Not more than the specified value.
		Appearance Attention:	There shall be no leakage of electrolyte.
			ge at abnormal situation only. It is not applicable to suc
		perpendicular directions. Vibration frequency ran Peak to peak amplitude Sweep rate Mounting method:	
4.10 Vibration test	Vibration test	4mm o	Within 30°
		Appearance o	To be soldered tems shall be tested: To intermittent contacts, open or short circuiting. To damage of tab terminals or electrodes. To mechanical damage in terminal. No leakage of electrolyte or swelling of the case. The markings shall be legible.

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		<condition></condition>	4.1. 1 4	- £-11 '	1141		
		The capacitor shall be tes		_	ondition	is:	
	Soldering temperature		: 245±3°C				
	Dipping depth		: 2mm	,			
4.11	Solderability	Dipping speed		: 25±2.5mm/	S		
	test	Dipping time		: 3±0.5s			
		<criteria></criteria>		A	CO50/	C.1 C 1	
		Coating quality		A minimum immersed	01 95%	of the surface l	being
				IIIIIIeiseu			
		<condition></condition>					
		Terminals of the capacito	r shall be in	nmersed into	solder	bath at 260 ± 5	5°C for10±
		1seconds or $400 \pm 10^{\circ}\text{C}$ for	or3 +1 second	s to 1.5~2.0n	nm from	the body of ca	pacitor.
		Then the capacitor shall b					
	Resistance to	for 1~2 hours before mea		the normal te	Imperati	are and normar	namany
4.12	solder heat	<criteria></criteria>					
7.12	test	Leakage current	Not	more than th	e specifi	ied value.	
		Capacitance Change	Wit	hin ±10% of	f initial	value.	
		tanδ	Not	more than th	e specifi	ied value.	
		Appearance				e of electrolyte	_
							-
		<condition></condition>					
		Temperature Cycle:Acco				ods, capacitor s	hall be
		placed in an oven, the con		rding as belov		D'	
		Temperature				Γime	
	(1)+20°C			≪3	Minutes		
	Change of	(2)Rated low temperature (-40°C) (-25°C)			30±2	Minutes	
4.13	temperature	(3)Rated high temperature (+105°C) 30 ± 2 Minutes					
	test	(1) to $(3)=1$ cycle, to	tal 5 cycle				
		<criteria></criteria>					
		The characteristic shall m					
		Leakage current		re than the sp			
		tanδ		re than the sp			
		Appearance	There s	hall be no lea	akage of	electrolyte.	
		<condition></condition>					
		Humidity Test:					
		According to IEC60384-4	4No.4.12 me	thods, capaci	itor shall	l be exposed fo	or 500 ± 8
		hours in an atmosphere of	f 90~95%R	H .at $40\pm2^\circ$	C, the ch	aracteristic cha	ange shall
		meet the following requir	rement.				
		< <u>Criteria></u>					
4.14 Damp heat	Leakage current		than the spec		ue.		
7.14	test	Capacitance Change		20% of initia			
		tanδ		than 120% of			
		Appearance	There shal	l be no leaka	ge of ele	ectrolyte.	

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The maximum permissible ripple current is the maximum A.C current at 120Hz and can be applied at maximum operating temperature Table-1	4.15	Vent test	Condition> The following test only apply to those products with vent products at diameter ≥∅6.3 with vent. D.C. test The capacitor is connected with its polarity reversed to a DC power source. Then a current selected from below table is applied. Table 3> Diameter (mm) DC Current (A) 22.4 or less 1 Over 22.4 The vent shall operate with no dangerous conditions such as flames or dispersion of pieces of the capacitor and/or case. Condition>
	4.16	permissible (ripple	The maximum permissible ripple current is the maximum A.C current at 120Hz and can be applied at maximum operating temperature Table-1 The combined value of D.C voltage and the peak A.C voltage shall not exceed the rated voltage and shall not reverse voltage. Frequency Multipliers: Coefficient (Hz) 50 120 300 1K 100k

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances			
	Cadmium and cadmium compounds			
Haavy matala	Lead and lead compounds			
Heavy metals	Mercury and mercury compounds			
	Hexavalent chromium compounds			
	Polychlorinated biphenyls (PCB)			
Chloinated	Polychlorinated naphthalenes (PCN)			
organic	Polychlorinated terphenyls (PCT)			
compounds	Short-chain chlorinated paraffins(SCCP)			
	Other chlorinated organic compounds			
Polybrominated biphenyls (PBB)				
Brominated .	Polybrominated diphenylethers(PBDE) (including			
organic	decabromodiphenyl ether[DecaBDE])			
compounds Other brominated organic compounds				
Tributyltin comp	ounds(TBT)			
Triphenyltin com	npounds(TPT)			
Asbestos				
Specific azo com	pounds			
Formaldehyde				
Beryllium oxide				
Beryllium copp	er			
Specific phthalat	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)			
Hydrofluorocarb	on (HFC), Perfluorocarbon (PFC)			
Perfluorooctane s	sulfonates (PFOS)			
Specific Benzotri	iazole			

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Attachment: Application Guidelines

1.Circuit Design

1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
 - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
 - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
 - a) At higher frequencies capacitance and impedance decrease while tanô increases.
 - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

(1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

(2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

(3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

(4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

1.4 Using Two or More Capacitors in Series or Parallel

(1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

(2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

1.5 Capacitor Mounting Considerations

(1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

(2)Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

(3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

(4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

 ϕ 6.3~ ϕ 16mm:2mm minimum, ϕ 18~ ϕ 35mm:3mm minimum, ϕ 40mm or greater:5mm minimum.

(5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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(6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about $1k\Omega$.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately $1k\Omega$.
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result.

2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 °C for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150°C for a maximum time of 2 minutes.

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2.6 Capacitor Handling after Solder

- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60℃ maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinyl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100°C temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a 1000Ω , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

